

PATENT

Docket No. JCLA4020

Date: 6-28-2001

Page 1

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

ATTENTION: APPLICATION BRANCH

Sir:

 \boxtimes

Transmitted herewith for filing is the patent application of

Inventors: K. T. Hsiao, Hao-Liang Lo, Li-Yang Yang

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For: TESTING ARCHITECTURE FOR A SEMICONDUCTOR MEMORY DEVICE

Enclose	ed are:
\boxtimes	Specification 11 pages.
	_5 Sheets of drawings
\boxtimes	Recordation Form Cover sheet with _2_ pages assignment.
	A certified copy of Taiwan Patent Application No dated _
\boxtimes	SIGNED declaration and power of attorney.

CLAIMS AS FILED									
FOR	NUMB	ER FII	LED :	NUMBE	R EXTRA	RATE	FEE		
Basic Fee					-	\$710.00	\$710.00		
Total Claims	5	_	20 =	. 0	×	\$18.00	\$0.00		
Independent Claims	2	_	3 =	. 0	×	\$80.00	\$0.00		
If application contains any r	\$270.00	\$0.00							
TOTAL FILING FEE							\$710.00		

A check in the amount of \$710.00 cover the filing fee is enclosed.

A check in the amount of \$40.00 to cover the assignment recording fee.

A duplicate copy of this sheet is enclosed.

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Docket No. : JCLA4020

Applicant(s) : K. T. Hsiao, Hao-Liang Lo, Li-Yang Yang

For : TESTING ARCHITECTURE FOR A SEMICONDUCTOR MEMORY DEVICE

"Express Mail" : EL 800897275 US

Mailing Label No.

Date of Deposit: June 28, 2001

I hereby certify that the accompanying

Transmittal in Duplicate; Specification <u>11</u> pages, <u>5</u> sheets of drawings; **SIGNED** Declaration and Power of Attorney <u>2</u> pages; Recordation Form Cover Sheet and Assignment <u>3</u> pages; Checks for Filing Fee(s); Return Prepaid Postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Michelle Chang